

**ABSTRACT OF THE DISCLOSURE**

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Provided is an An apparatus is provided for detecting a correlation of samples with a spread code comprising: an L-chip accumulator which inputs the samples to generate and output an intermediate correlation signal; memories as many as M memories, each of which stores L x M samples of the intermediate correlation signal as many as L x N; an adder which has input terminals as many as M and inputs from each of the input terminals the intermediate correlation signal which is outputted from the L-chip accumulator or the intermediate correlation signal which is outputted from a corresponding memory among the memories; and a controller which supplies the intermediate correlation signal outputted from the L-chip accumulator to the memories as many as M and to the M input terminals as many as M of the adder in rotation with a unit of  $L \times N$  samples, and reads, and supplies to each of the input terminals of the adder, the intermediate correlation signal which has been stored in each of the memories M-1 times; wherein an output of the adder is outputted as an correlation signal outputted from the apparatus.